

A METHOD AND APPARATUS FOR MODELING A WEB SERVER

CROSS-REFERENCES TO RELATED APPLICATIONS

5 This application claims the benefit of U.S. Provisional Application No. 60/113,816, filed December 23, 1998, entitled "An Analytical Model For A Web Server," the entire disclosure of which is incorporated by reference herein.

FIELD OF THE INVENTION

10 The invention relates to communication devices in general. More particularly, the invention relates to a method and apparatus for building an analytic model for a web server.

BACKGROUND OF THE INVENTION

15 Over the past few years, the World Wide Web ("Web") has seen tremendous growth, which is unlikely to slow down in the near future. The explosion of Internet Commerce and Telephony services brought the heretofore distinct Internet space into the realm of traditional telecommunications, and ensured that the Web will remain at

the center of mainstream communications. At the heart of most Internet Commerce and Telephony service offerings is the Web server. Based on the Hypertext Transfer Protocol (HTTP) over the Transaction Control Protocol/ Internet Protocol (TCP/IP), Web servers are expected to perform millions of requests per day at an acceptable

- 5 Quality of Service (QoS) in terms of transaction throughput (connect and error rates) and latency (connect and response times) experienced by end users. To cope with the increasing volume of transaction requests, as well as the increasing demands of real-time communications, a thorough understanding of the performance capabilities and limitations of Web servers is crucial.

- 10 Web server performance is a complicated interplay between a variety of components, such as server hardware platform, Web server software, server operating system, network bandwidth, workload characteristics, disk caching, etc. Experience has shown that the performance of Web servers can be increased tremendously by the proper tuning of the server components. To properly configure 15 these different components, however, it is crucial to understand how these components interact and how they impact user-perceived end-to-end performance.

- To compare the performance of Web server platforms, numerous benchmarking tools have been brought to the market. The existing benchmarking tools, however, have a number of drawbacks. First, these tools consider the Web 20 server as a “black box”, and as such fail to provide insight into how the individual components impact end-to-end performance. Second, benchmarking is extremely time-consuming, and experiments must be performed for many different workload

scenarios. Third, benchmarking tools typically run on a workstation connected to a Web server via a high-speed LAN, whereas in practice Internet-based end users are connected to the Web server via low-speed modems. Therefore, applying the outcomes of benchmarking experiments as an estimation of the actual Web server 5 performance capabilities may lead to erroneous performance predictions.

In view of the foregoing, it can be appreciated that a substantial need exists for a method and apparatus which can evaluate and compare the performance of Web server platforms in an efficient and effective manner in order to solve the aforementioned problems.

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SUMMARY OF THE INVENTION

The embodiments of the invention include a method and apparatus for modeling a web server. The server is modeled by identifying a plurality of sub-systems for the server. Each sub-system is represented as a queue, with each queue 15 operably coupled to one another. An arrival rate and a service time is iteratively adjusted for each queue to account for performance by the other queues.

With these and other advantages and features of the invention that will become hereinafter apparent, the nature of the invention may be more clearly understood by reference to the following detailed description of the invention, the 20 appended claims and to the several drawings attached herein.

BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a communication system model which illustrates the flow of a typical HTTP transaction (file request) through a Web server.

FIG. 2 depicts the detailed feedback mechanism within the HTTP sub-
5 system.

FIG. 3(a), 4(a) and 5(a) depict the effective server throughput λ_{conn} as a function of the transaction request rate (TRR) λ_{file} under different workload and network scenarios.

FIG. 3(b), 4(b) and 5(b) show the average end-to-end service time $\tau_{\text{conn}} + \tau_{\text{resp}}$
10 as a function of the TRR under different workload and network scenarios.

FIG. 6 illustrates the sub-elements of a Web server in accordance with one embodiment of the invention.

FIG. 7 illustrates a finite queue, finite server queueing system in accordance with one embodiment of the invention.

15 FIG. 8 illustrates a block flow diagram of an algorithm in accordance with one embodiment of the invention.

DETAILED DESCRIPTION

The performance of a Web server is a complex interplay between the variety
20 of components, for which previous performance predictions could only be obtained via time-consuming simulation. To overcome this problem, the embodiments of the invention present a model that captures key performance limiting factors of an HTTP server, while keeping the model simple enough to be exactly analyzable. The

results lead to fast-to-evaluate approximations of the key performance measures of the Web server, like the server throughput, the end-to-end service time, and the blocking probabilities. The approximations are shown to be accurate in a variety of realistic scenarios. Based on these observations, the approximate predictions form

- 5 an excellent basis for the development of Decision Support Systems for the configuration tuning and sizing of Web servers.

The embodiments of the invention are useful for the modeling and analysis of the impact of the different components on Web server performance. Accordingly, the embodiments of the invention describe a new analytic model for Web servers.

- 10 HTTP transactions proceed along a number of phases in successive order.

Therefore, the HTTP flows within a Web server can be described by a tandem queueing model consisting of the following sub-models:

- a multi-server, zero-buffer blocking model for the TCP/IP connection setup phase;
- 15 • a multi-server, finite-buffer queueing/blocking model for the HTTP server processing; and
- a multi-server, infinite-buffer queueing model for the network I/O sub-system.

- The embodiments of the invention provide a tractable analytic model of the intricate dependencies and interactions between the sequential Web server sub-systems by decoupling the sub-systems, while at the same time accurately capturing the impacts of “downstream” congestion. The resulting model consists of a sequence of independent, yet coupled, queues, where the arrival rates and service

times at each node are iteratively adjusted to account for downstream performance.

The embodiments of the invention are described using the following notations:

- Let $M(\lambda)/M(\tau)/N/Q$ denote the classical Markovian model with N servers, finite buffer size Q , First Come First Serve (FCFS) discipline, Poisson arrivals (rate λ), and exponential service times (mean τ);
- 5 • Denote by π the probability that all servers are busy, and denote by β the probability that all servers and buffers are occupied.
- Let n be the average number of occupied servers, let q be the average queue length, and let ω be the average waiting time in queue.
- 10 • Note that when $Q=0$, β represents the Erlang B (“blocked calls cleared”) formula, and when $Q=\infty$, π represents the Erlang C (“blocked calls delayed”) formula.

The embodiments of the invention can be implemented using a processor-based personal computer (PC) system and computer program segments stored in memory that when executed by the processor performs the functionality described herein.

The processor can be any processor capable of providing the speed and functionality of the embodiments of the invention. For example, processor 202 could include the Pentium® family of processors made by Intel Corporation, or the 68XXX family of processors made by Motorola. Further, the computer program segments can be stored on any machine readable medium capable of storing instructions adapted to be executed by a processor. Some examples of such machine readable media

include, but are not limited to, read-only memory (ROM), random-access memory (RAM), programmable ROM, erasable programmable ROM, electronically erasable programmable ROM, dynamic RAM, magnetic disk (e.g., floppy disk and hard drive), optical disk (e.g., CD-ROM), and any other device that can store digital information. In one embodiment, the instructions are stored on the medium in a compressed and/or encrypted format. As used herein, the phrase “adapted to be executed by a processor” is meant to encompass instructions stored in a compressed and/or encrypted format, as well as instructions that have to be compiled or installed by an installer before being executed by the processor. In addition, although this embodiment of the invention implements the functionality of these modules in software, it can be appreciated that the functionality of these modules may be implemented in hardware, software, or a combination of hardware and software, using well-known signal processing techniques.

It is worthy to note that any reference in the specification to “one embodiment” or “an embodiment” means that a particular feature, structure, or characteristic described in connection with the embodiment is included in at least one embodiment of the invention. The appearances of the phrase “in one embodiment” in various places in the specification are not necessarily all referring to the same embodiment.

Referring now in detail to the drawings wherein like parts are designated by like reference numerals throughout, there is illustrated in FIG. 1 a communication system model which illustrates the flow of a typical HTTP transaction (file request) through a Web server. As indicated in FIG. 1, the transaction proceeds through three

successive phases: (1) TCP/IP connection setup between a client personal computer (PC) 102 and a Web server 104; (2) HTTP file access; and (3) network Input/Output (I/O) processing. Each transaction flow is discussed in detail below. It is worthy to note that only "static" file access is used for clarity in describing the principles of the 5 embodiments of the invention. It can be appreciated, however, that the analytical model can be modified to include a "dynamic" script execution sub-system and still fall within the scope of the invention. Such an embodiment is depicted, for example, in FIG 6 as well as in the flow diagram in FIG. 8.

FIG. 1 models three different sub-systems of a Web server 104, with each sub-10 system corresponding to a particular phase of a typical HTTP transaction. The sub-systems include a TCP/IP sub-system, an HTTP sub-system, and an I/O subsystem.

The TCP/IP sub-system consists of a finite Listen Queue (LQ) 106 of size N_{tcp} served by a "listener" daemon (in the case of HTTP traffic, "httpd"). Prior to data transmission, a two-way connection (socket) must be established between a client 15 personal computer (PC) 102 and Web server 104. It is assumed that incoming connection requests form a Poisson process with rate λ_{file} (in files/second). Incoming requests remain in the TCP LQ 106 until the socket is established via a "three-way handshake" as follows:

- The client PC 102 TCP stack sends a connect request (SYN) to Web server 104;
- 20 • If there is an available slot in TCP LQ 106, then the request occupies one slot, and the listener daemon sends an acknowledgment (SYN-ACK) to client PC

102; otherwise, the connection request is rejected, and Web server 104 sends a
“connection refused” message to client PC 102; and

- Upon receipt of Web server 104's acknowledgment (SYN-ACK), client PC 102
acknowledges Web server 104's SYN with an ACK. The two-way TCP socket
5 connection is now established.

Client PC 102 follows the ACK with an HTTP request (e.g., GET, POST, HEAD).

Upon receipt of the HTTP request, HTTP daemon 110 accepts the request (discussed
in the next section), and the request is removed from TCP LQ 106. Eventually, after
the HTTP request is processed and the last I/O buffer block is transmitted, Web

10 server 104 closes its half of the TCP socket (FIN). Upon receipt of the FIN, client
PC 102 sends an ACK, followed by a FIN. Upon receipt of the FIN, Web server 104
sends an ACK, and the socket is closed.

The actual daemon service time to process the connect() and accept()
requests is negligible; therefore, the fact that there is one “server” is inconsequential.

15 Rather, TCP LQ 106 behaves like a group of N_{tcp} separate servers, where the
“holding” time for each request in TCP LQ 106 consists of one network round-trip
time (RTT). It is assumed that the TCP holding time (network RTT) is
exponentially distributed with mean τ_{tcp} (in seconds). Thus, the TCP/IP sub-system
can be modeled as an $M(\lambda_{file}) / M(\tau_{tcp}) / N_{tcp} / 0$ blocking system.

20 Incoming requests that arrive when all N_{tcp} LQ slots are occupied are blocked
(“connection refused”), and retry with fixed probability π_{retry} . Reattempts are
accounted for by inflating the offered load, as shown in FIG 2. The total offered

load A_{tcp} (including reattempts) is given by $A_{tcp} = \lambda_{file}\tau_{tcp} / (1 - \pi_{retry} \beta_{tot})$, where $\beta_{tot} = \beta_{tcp} + \beta_{http} - \beta_{tcp}\beta_{http}$ is the total (TCP + HTTP) LQ blocking of incoming requests.

The probability of blocking β_{tcp} at the TCP LQ, the average number n_{tcp} of occupied TCP LQ slots, and the effective file request rate λ_{http} at the downstream HTTP sub-

5 system then follow directly from basic M/M/N/0 results.

It is worthy to note that the impact of “downstream” congestion on the TCP/IP sub-system is captured in the calculation of the total offered load A_{tcp} (through the reattempt stream), which in turn impacts the calculations of the TCP LQ blocking β_{tcp} and total (TCP + HTTP) LQ blocking β_{tot} .

- 10 The HTTP sub-system consists of a LQ 108 of size Q_{http} served by one or more multi-threaded HTTP daemons (httpd) 110 with N_{http} separate server threads. After the TCP connection is established and the HTTP request is received, the HTTP daemon issues an accept() request. If an HTTP server thread is available, then the request goes directly into service. Otherwise, if an HTTP LQ 108 slot is available, 15 then the request waits for a server thread in FCFS order in HTTP LQ 108. Otherwise, the HTTP daemon rejects the request (“connection refused”), and the TCP connection is torn down.

- Once the HTTP request enters service, the server thread reads the blocks of the requested file (either from cache or disk), and writes them to the I/O sub-system 20 as follows: If a network output buffer is available, then the thread immediately begins to write the file into the output buffer. Otherwise, the thread waits (blocks) for an output buffer in FCFS order. Once an output buffer becomes available, the

server thread begins to write the file into the buffer. If the file fits entirely into the buffer in one pass, the thread is released. Otherwise, the server thread waits (blocks) until the entire output buffer is drained, at which point the thread resumes writing the file (to the same output buffer). Eventually, the remainder of the file fits entirely 5 into the buffer in the last pass, and the server thread is released and becomes available to handle another request.

The average thread holding time τ_{http} consists of the time τ_{read} to access each of the file blocks (from cache or disk), plus the time ω_{ioq} that the thread is blocked waiting for an output buffer to become available (in the case that all output buffers 10 are occupied), plus the time ω_{buf} that the thread is blocked waiting to refill the output buffer (in the case that the file does not fit entirely in one pass). The average file access time τ_{read} is given by $\tau_{\text{read}} = n_{\text{file}} [\pi_{\text{chr}} \tau_{\text{cache}} + (1-\pi_{\text{chr}}) \tau_{\text{disk}}]$, where n_{file} is the file size (in blocks), π_{chr} is the probability that the block resides in cache, τ_{cache} is the average cache access time per block, and τ_{disk} is the average disk access time per 15 block. Assuming that file size is geometrically distributed, the average waiting time ω_{buf} to refill the output buffer is given by $\omega_{\text{buf}} = N_{\text{io}} \tau_{\text{io}} \pi_{\text{buf}} / (1-\pi_{\text{buf}})$, where τ_{io} is the average block service time.

It is assumed that incoming HTTP requests form a Poisson process with rate λ_{http} , each with an exponentially distributed thread holding time with mean τ_{http} . The 20 HTTP sub-system is modeled as an $M(\lambda_{\text{http}}) / M(\tau_{\text{http}}) / N_{\text{http}} / Q_{\text{http}}$ queueing system. Incoming requests that arrive when all Q_{http} LQ slots are occupied are blocked (“connection refused”), and retry with probability π_{retry} . As before, reattempts are

accounted for by inflating the offered load (see FIG. 2). The total offered load A_{http} (including reattempts) is given by $A_{\text{http}} = \lambda_{\text{file}} \tau_{\text{http}} (1 - \beta_{\text{tcp}}) / (1 - \pi_{\text{retry}} \beta_{\text{tot}})$. The probability of blocking β_{http} at the HTTP LQ, the average number q_{http} of occupied HTTP LQ slots, the average waiting time ω_{http} in the HTTP LQ, the average number 5 n_{http} of occupied HTTP threads, and the effective file request rate λ_{buf} at the I/O controller sub-system then follow directly from basic M/M/N/Q results.

It is worthy to note that the impact of “downstream” congestion on the HTTP sub-system (in terms of both I/O buffer size and occupancy) is captured in the calculation of the average thread holding time τ_{http} .

10 The I/O sub-system consists of a finite number N_{buf} of network buffers 114, served by a single I/O controller (IOC) 116 in round-robin (polling) fashion. Once the server thread writes the file into an output buffer 114, IOC 116 begins to write the blocks over the TCP connection as follows: IOC 116 sends one TCP window’s worth of data to client PC 102, and waits for an acknowledgment. Once the ACK is 15 received and IOC 116 returns to an output buffer 114, another window’s worth of data is sent to client PC 102, and so on, until the entire file is written.

The TCP flow control mechanism described above is a complicated interplay between the server maximum segment size (MSS), network message transfer unit (MTU), window size, “slow start” algorithm, and so forth. The TCP window size 20 between Web server 104 and client PC 102 using a 28.8Kbps modem typically ranges between an initial slow-start value of 1 MTU, and an optimal value of 3-4 MTUs. A typical scenario could proceed as follows: Web server 104 sends 1 MTU (receives ACK from client PC 102), sends 2 MTUs (receives ACK), sends 4 MTUs (encounters network congestion and backs off), resends 2 MTUs (receives ACK), 25 and so forth. Therefore, a typical flow to send 10 MTU’s worth of data could consist of five cycles with 1, 2, 2, 2, and 3 MTUs, with an average of 2 MTUs per cycle. For simplicity, it is assumed that each block of a file consists of 2 MTUs, and

that the TCP window size is 2 MTUs on average. Therefore, IOC 116 sends an average of 1 block per cycle.

It is assumed that incoming requests form a Poisson process with rate λ_{buf} , each with an exponentially distributed buffer service time with mean τ_{buf} . The I/O 5 sub-system is modeled as an $M(\lambda_{buf}) / M(\tau_{buf}) / N_{buf} / \infty$ queueing system, where the average buffer service time τ_{buf} captures the impact of the round-robin I/O controller. Specifically, the average service time τ_{iob} for each block is determined as follows: Assume that a server thread places the requested file in the output buffer at an arbitrary time t . The time to service the first block then consists of the residual cycle 10 time τ_{ioc} for IOC 116 to arrive at the buffer in question, plus the time τ_{link} to put the block on the output link, plus the modem time τ_{mod} to read the block, plus the network RTT τ_{tcp} to send the block and return the ACK. Once the ACK arrives at Web server 104 (at time $t + \tau_{iob}$), the window is opened, and the next block is available for service (and so on). Thus, each block sees the same average service 15 time $\tau_{iob} = \tau_{ioc} + \tau_{link} + \tau_{tcp} + \tau_{mod}$, and the average buffer service time $\tau_{buf} = n_{file}\tau_{iob}$.

The residual cycle time τ_{ioc} consists of the time to service all residual non-empty buffers whose TCP windows are open. Assuming that the number of non-empty buffers is uniformly distributed, the average residual cycle time $\tau_{ioc} = 20 \frac{1}{2}n_{buf}\pi_{win}\tau_{link}$, where n_{buf} is the average number of occupied buffers and π_{win} is the probability that the ACK has arrived since the last IOC visit (hence, the TCP window is open). Applying basic M/M/N/ ∞ results, the average number of occupied buffers $n_{buf} = \lambda_{buf}n_{file}\tau_{iob}$. Solving for τ_{iob} yields $\tau_{iob} = (\tau_{link} + \tau_{tcp} + \tau_{mod}) / (1 - \frac{1}{2}\lambda_{buf}n_{file}\pi_{win}\tau_{link})$. The probability that the window is open equals the probability that the ACK time is less than the complete cycle time. Assuming that the network 25 RTT is exponentially distributed with mean τ_{tcp} , then $\pi_{win} = 1 - \exp\{-(\tau_{mod} - n_{buf}\pi_{win}\tau_{link})/\tau_{tcp}\}$ for $n_{buf}\pi_{win}\tau_{link} \geq \tau_{mod}$. Since $\pi_{win} \rightarrow 1$ under heavy server load, it can be conservatively assumed that $\pi_{win} = 1$. Thus, $\tau_{iob} = (\tau_{link} + \tau_{tcp} + \tau_{mod}) / (1 - \frac{1}{2}\lambda_{buf}n_{file}\tau_{link})$.

Finally, the probability π_{ioq} of finding all output buffers occupied, the average number q_{ioq} of server threads that are blocked waiting (queueing) for a network

output buffer, and the average time ω_{ioq} that the thread is blocked (queued) then follow directly from basic M/M/N/ ∞ results.

- The system of equations defined above can be solved iteratively as follows: (1) compute the upper bound (worst case) I/O sub-system performance by assuming that
- 5 no requests are blocked in the TCP/IP or HTTP sub-systems, then compute the upper bound (worst case) TCP/IP and HTTP sub-system performance by assuming the value for the I/O buffer waiting time computed above; (2) compute the lower bound (best case) I/O performance by assuming the values for blocking computed above, then compute the lower bound (best case) TCP/IP and HTTP performance by
- 10 assuming the value for I/O waiting time computed above; and (3) repeat steps (1) and (2) to generate successively tighter bounds until convergence.

Once the iteration is complete, the end-to-end, user-perceived performance metrics are computed. First, the average user-perceived file “connect” time consists of the TCP/IP service time, plus the HTTP waiting time, $\tau_{\text{conn}} = \tau_{\text{tcp}} + \omega_{\text{http}}$. The

15 average user-perceived file “response” time consists of the HTTP service time, plus the I/O waiting and service times, $\tau_{\text{resp}} = \tau_{\text{read}} + \omega_{\text{ioq}} + \tau_{\text{buf}}$. The end-to-end service time is then the sum of connect + response times, $\tau_{\text{conn}} + \tau_{\text{resp}}$. The effective file connect rate $\lambda_{\text{conn}} = \lambda_{\text{file}}(1-\beta_{\text{tcp}})(1-\beta_{\text{http}})/(1-\pi_{\text{retry}}\beta_{\text{tot}})$, and the effective file error (blocking) rate $\lambda_{\text{error}} = \lambda_{\text{file}} - \lambda_{\text{conn}} = \lambda_{\text{file}}(1-\pi_{\text{retry}})\beta_{\text{tot}}/(1-\pi_{\text{retry}}\beta_{\text{tot}})$.

20 The computation time required to obtain the approximations according to the steps outlined above is typically less than a second, whereas to obtain accurate predictions of the performance of Web server 104 by means of conventional techniques such as simulation on a modern work station is on the order of hours.

Various numerical experiments were used to validate the accuracy of the

25 analytic approximation used in the embodiments of the invention. For a number of realistic scenarios the server throughput and the average end-to-end service time were computed by means of both simulation and analytic approximation.

For example, consider the following realistic scenario wherein a Web server is connected to the Internet via a T1.5 (1.544 Mbps) connection. Clients are connected

to the Internet via 28.8 Kbps modems. The RTT for ACKs to move back and forth between the server and client is exponentially distributed with mean 250ms. The Web server runs on a single processor. The size of the TCP LQ is 1024, the size of the HTTP LQ is 128, and the number of HTTP worker threads is 128. The MTU size is 512B, and no reattempts are allowed. The total processor time required by an HTTP thread to perform the HTTP layer processing is 15ms per (1KB) block. The average output file size is 4KB (thus, the required processor time is 60ms/file). The number of I/O buffers is 256. The scenario described above is typical and realistic for clients connected to a Web server via the Internet.

10 To assess the accuracy of the analytic approximation, the average server throughput and the average end-to-end service time for a variety of transaction request rates were computed. FIG. 3(a) depicts the effective server throughput λ_{conn} as a function of the transaction request rate (TRR) λ_{file} , and FIG. 3(b) shows the average end-to-end service time $\tau_{\text{conn}} + \tau_{\text{resp}}$ as a function of the TRR.

15 The results in Figures 3(a) and 3(b) yield a number of interesting observations. First, it is observed that for increasing values of TRR, the throughput increases linearly up to some threshold T^* , and remains constant when TRR exceeds this threshold. It is readily verified that T^* corresponds to the performance limitation of the HTTP layer processing. More precisely, since the amount of CPU time required 20 is 0.06 sec/file, and the number of processors is 1, the processor saturates when the throughput $T^* = 1/0.06 \approx 17$ transactions/sec (Tps). As for end-to-end delay, FIG. 3(b) shows that the service time is fairly constant for TRR values smaller than T^* and “jumps” to a higher constant when TRR exceeds T^* . In those cases blocking at the HTTP sub-system occurs, leading to connection rejections. As for the accuracy 25 of the analytic approximations, it is observed that the approximations are highly accurate for all values of TRR.

Next, consider what happens if the HTTP processing bottleneck is alleviated. This may be done, e.g., by upgrading the processor or improving caching performance. Assume that the total processor time required per file is decreased

from 60 ms to 10 ms. For this scenario, Figures 4(a) and 4(b) depict the server throughput and response times as a function of TRR. Figure 4(a) shows that by decreasing the processor time required by a factor of 6 (from 60 to 10 ms/file), the maximum throughput has increased by a factor of only about 3, suggesting that the 5 bottleneck has moved to another sub-system. In fact, one may verify that the transport capability of the T1 connection (with a bandwidth of 1.544 Mbps) has become the limiting factor, restricting the throughput to $T^* = (1.544\text{Mbps})/(4\text{KB}) \approx 47 \text{Tps}$. During the simulations, blocking was also observed at the HTTP sub-system for TRR values above T^* . This is caused by the fact that saturation of the 10 network connection leads to “flooding” of the I/O buffers, which in turn implies that HTTP worker threads must wait for I/O buffers to become available, causing flooding of the HTTP sub-system and finally buffer overflow at the HTTP LQ. Again, it is observed that the approximation is reasonably accurate in the cases considered.

15 The models considered above are typical for Internet Web servers. For comparison, consider a situation where a Web server is used in an Intranet environment. The same model is considered as used for Figures 4(a) and 4(b), except that clients are connected to the server via a 100Mbps LAN. It is assumed that the RTT for ACKs to move back and forth between the server and client, plus 20 the time to read an incoming block at the client side, is exponentially distributed with an average of only 1ms. FIGS. 5(a) and 5(b) show the results.

The results in FIGS. 5(a) and 5(b) show that the maximum throughput has increased to about 100 Tps. Since the connection bandwidth in the LAN environment considered here is much larger than in the Internet environment (where 25 we assumed T1 connections), the draining of the I/O buffer is much faster, and the HTTP sub-system has again become the performance bottleneck. It was also observed that the end-to-end delay decreased dramatically compared to the results in the Internet environment. As was the case in the previous examples, the approximations match remarkably well with the simulation results.

The numerical results discussed in this section demonstrate that the fast approximate performance predictions based on the analytic model used in the embodiments of the invention closely approximate the results obtained via simulation using conventional techniques, but at a much faster rate.

- 5 The numerical results in the previous section reveal an interesting difference between Web servers used in an Internet versus Intranet environment. In an Intranet application, the I/O buffers are typically drained over a high-speed LAN and at a much higher rate than in an Internet environment, where the clients are usually connected to the server via a low-speed modem. More precisely, in the Intranet case,
10 the RTT for an output file block to be sent and acknowledged (i.e., τ_{lob}) is typically on the order of a few milliseconds, whereas in the Internet τ_{lob} is usually on the order of hundreds of milliseconds. Similar observations hold for the average duration τ_{tcp} of the TCP connection set-up phase. Consequently, for Internet Web servers the I/O sub-system is more likely to be a performance bottleneck than for Intranet Web
15 servers.

The above observation also addresses a common misinterpretation of the results obtained via most commercially available Web server benchmarking tools. These tools typically generate transaction requests at a high rate and monitor the responsiveness (delay, throughput) of the Web server. In the absence of a large
20 number of modems available for testing, the benchmarking tools emulate a large number of clients and are typically connected to the Web server over a high-speed LAN connection. Consequently, the commercial benchmarking tools only put the HTTP sub-system to a test, while the TCP and the I/O sub-systems are not stressed. Therefore, although such an approach may certainly be useful for assessing the
25 relative performance of different Web servers, using the absolute results obtained via a LAN-connected benchmarking tool as predictions of the responsiveness of the Web servers in the Internet may lead to strongly erroneous (overly optimistic) performance predictions.

The queueing models for the different sub-systems are coupled in the sense

that the impact of “downstream” congestion is captured in the arrival rates and/or service times at each sub-model as discussed in more detail above. In this way, the coupling of the sub-models reflects the dependence between the performance of the different sub-systems. To discuss the impact of the coupling of the sub-models in

5 some more detail, suppose that the network RTT (i.e., τ_{tcp}) is temporarily increased due to congestion in the Internet. As a consequence, the TCP acknowledgments (from the client to the server) of the receipt of file blocks are delayed, so that the I/O buffers are drained at a lower rate (i.e., τ_{iob} increases). This implies that I/O buffers become available to the HTTP worker thread at a lower rate, so that HTTP worker
10 threads may have to wait (idle) for I/O buffers to become available (i.e., τ_{http} increases). This, in turn, implies that the effective throughput of the HTTP worker threads decreases, which may lead to overflow of the HTTP LQ and hence, to blocking of the incoming transaction requests. In this way, congestion in the Internet may manifest itself as blocking at the HTTP sub-system.

15 FIG. 6 illustrates the sub-elements of a Web server in accordance with one embodiment of the invention. FIG. 6 shows four sub-systems:

1. TCP 602, responsible for network and transport layer Transaction Control Protocol processing;
2. HTP 604, responsible for application layer Hypertext Transfer Protocol processing;
3. ASC 606, responsible for dynamic Active Script Component layer processing; and
4. IOC 608, responsible for network Input/Output Control processing.

In accordance with one embodiment of the invention, each component (sub-system)
25 shown in FIG. 6 is modeled as a finite-buffer, finite-server queueing system (shown in FIG. 7). The following notation is convenient to describe the inputs (for clarity, only the TCP-specific inputs are listed; comparable inputs exist for the HTP, ASC, and IOC sub-systems):

- λ_{tcp} denotes the initial arrival rate (in files/second) to the TCP sub-system;
- τ_{tcp} the service time (in seconds) at the TCP sub-system;
- Q_{tcp} the total number of available buffers in the TCP sub-system;
- N_{tcp} the total number of available servers in the TCP sub-system;
- 5 π_{retry} the probability of reattempt by an arrival finding all servers and buffers occupied;
- λ_{file} the initial arrival rate (in files/second) offered to the system.

The following notation is convenient to describe the outputs (again for clarity, only
10 the TCP-specific inputs are listed):

- A_{tcp} denotes the total arrival rate to the TCP sub-system (including reattempts);
- S_{tcp} the total TCP server holding time including delays to enter the next
15 sub-system;
- q_{tcp} the average number of occupied buffers in the TCP sub-system;
- n_{tcp} the average number of occupied servers in the TCP sub-system;
- π_{tcp} the probability that all servers are occupied (queueing);
- β_{tcp} the probability that all servers and all buffers are occupied (blocking);
- 20 w_{tcp} the average waiting time (in seconds) in a TCP buffer prior to service;
- τ_{conn} the average time from when the initial request arrives at the TCP
sub-system until it enters service at the HTP sub-system (server connect
time);
- τ_{resp} the average time from when the request enters service at the HTP

sub-system until the requested file departs the IOC sub-system (server response time);

λ_{conn} the throughput rate (in files/second) of successful (served) requests;

λ_{error} the throughput rate (in files/second) of unsuccessful (denied) requests.

5

- FIG. 8 illustrates a block flow diagram of an algorithm in accordance with one embodiment of the invention. Using the above notation, an algorithm 800 is solved iteratively using the following steps. At step 802, input variables are inputted from a standard input or file: a_{file} , TCP_s , TCP_r , $TCP_Q = 0$, TCP_N , HTP_s , HTP_r ,
 10 HTP_Q , HTP_N , ASC_s , ASC_r , ASC_Q , ASC_N , IOC_s , IOC_r , $IOC_Q = \text{inf}$,
 IOC_N . The output variables are preset at step 804 as follows: $TCP_p = 0$, $TCP_b = 0$, $HTP_p = 0$, $HTP_b = 0$, $ASC_p = 0$, $ASC_b = 0$, $IOC_p = 0$, $IOC_b = 0$,
 15 $TCP_a = HTP_a = a_{\text{file}}$, and $ASC_a = IOC_a = a_{\text{file}}$. The upper bound IOC performance is computed at step 806 by assuming that no arrivals are blocked in the
 IOC ,
 20 TCP , HTP , or ASC sub-systems. This is accomplished by setting $IOC_A = a_{\text{file}}$ and solving the system of equations for IOC_n , IOC_q , IOC_p , IOC_b , and IOC_w . The performance of the upper bound TCP , HTP , and ASC is computed at step 808 based on the value of IOC_w computed in step 806. This is accomplished by setting
 $ASC_S = ASC_s + IOC_w$ and solving the equations for TCP_A , TCP_n , TCP_b ,
 25 HTP_A , HTP_n , HTP_q , HTP_p , HTP_b , HTP_w , ASC_A , ASC_n , ASC_q ,
 ASC_p , ASC_b , and ASC_w . The lower bound IOC performance is computed at step 810 based on the values of TCP , HTP , and ASC blocking computed in step 808. This is accomplished by setting $IOC_A = ASC_A(1-ASC_b)$ and solving the
 $equations for IOC_n$, IOC_q , IOC_p , IOC_b , and IOC_w . The lower bound TCP ,
 HTP , and ASC performance is computed at step 812 based on the value of IOC_w computed in step 810. This is accomplished by setting $ASC_S = ASC_s + IOC_w$ and solving the equations for TCP_A , TCP_n , TCP_b , HTP_A , HTP_n , HTP_q ,
 HTP_p , HTP_b , HTP_w , ASC_A , ASC_n , ASC_q , ASC_p , ASC_b , and ASC_w . Steps 810 and 812 are then repeated at step 814 until the values for all TCP , HTP ,

ASC, and IOC performance measures converge. The system-wide output variables are computed at step 816 as follows: $T_{conn} = TCP_s + HTP_w$, $T_{resp} = HTP_s + ASC_w + ASC_S + IOC_s$, $R_{conn} = IOC_A$, and $R_{error} = a_file - IOC_A$. The algorithm is then complete.

- 5 Although various embodiments are specifically illustrated and described herein, it will be appreciated that modifications and variations of the present invention are covered by the above teachings and within the purview of the appended claims without departing from the spirit and intended scope of the invention. For example, the model presented herein describes the traffic flow for
10 HTTP version 1.0 (currently the standard). Under HTTP 1.0, a TCP connection must be established for each transaction (file) request. To overcome this inefficiency, the next HTTP standard (1.1) makes use of so-called persistent connections, i.e., TCP connections that are kept open to serve multiple HTTP transaction requests. It can be appreciated that the principles described herein can be
15 extended to incorporate the impact of persistent connections and still fall within the scope of the invention. In another example, since pages with dynamic content are becoming increasingly popular, some servers (e.g., Microsoft's IIS server) implement dedicated processes/threads to handle dynamic content (script execution), with different thread and object scope models. It can be appreciated that the principles
20 described herein can be used to understand the impact of thread scope and object scope on the performance of the Web server and still fall within the scope of the invention.